

JC932 U.S. PTO  
09/687600  
10/12/89

Class	Subclass
ISSUE CLASSIFICATION	



PATENT NUMBER

U.S. UTILITY PATENT APPLICATION

561  
5611

O.I.P.E.  
SCANNED GTH Q.A.

PATENT DATE

APPLICATION NO. 09/687600	CONT/PRIOR D	CLASS 083	SUBCLASS 467.	ART UNIT 3724	EXAMINER CHOI
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Tolson & Lipson

Integrated circuit package assembly

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PTO-2040  
12/89

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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<input checked="" type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input checked="" type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent No. <u>6,08,164</u> .	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	

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(Rev. 6/98)

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